

Hardware Configuration and Definition (HCD) for z/OS

Kód kurzu: ES96G

Learn to work with the Hardware Configuration Definition (HCD) function for z/OS, and to plan and initiate dynamic reconfiguration of your zSeries hardware environment. Learn to use the HCD dialogs of z/OS to create an Input/Output (I/O) configuration and dynamically alter the I/O configuration. Learn about the creation of an I/O Configuration Dataset (IOCDs) and various reports that HCD can build. Use a z/OS system to reinforce lecture topics and to practice working with the HCD dialogs. Hands-on lab projects may be done in teams depending on the number of attendees and location.

Pobočka	Dní	Katalógová cena	ITB
Praha	4	46 500 Kč	0
Bratislava	4	1 980 €	0

Všetky ceny sú uvedené bez DPH.

Termíny kurzu

Dátum	Dní	Cena kurzu	Typ výučby	Jazyk výučby	Lokalita
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Pre koho je kurz určený

This course is for people responsible for maintaining the I/O configuration contained in the input/output data files (IODFs) and input/output configuration data sets (IOCDs) at their z/OS installation.

Čo Vás naučíme

Describe new zSeries processor technology
Code new zSeries processors (z9 to z196)
Code FICON channels and FICON CTCs
Code Coupling Facilities (CF) and CF links
Code cascaded FICON Directors
Create an IODF work file on a z processor from scratch
Use CHPID mapping tool to create a validated work IODF
Use work IODF and create a production IODF
Perform Dynamic I/O changes on a real z/OS system
Build a LOADxx parmlib member for initial program load (IPL)
View configuration graphically
Create appropriate configuration reports

Požadované vstupné znalosti

You should have:

- A basic knowledge of z/OS and I/O configuration

This knowledge can be developed on the job, or by taking Fundamental System Skills in z/OS (ES10A).

Študijné materiály

Príručka ku kurzu firmy IBM podľa programu kurzu.

GOPAS Praha
Kodaňská 1441/46
101 00 Praha 10
Tel.: +420 234 064 900-3
info@gopas.cz

GOPAS Brno
Nové sady 996/25
602 00 Brno
Tel.: +420 542 422 111
info@gopas.cz

GOPAS Bratislava
Dr. Vladimíra Clementisa 10
Bratislava, 821 02
Tel.: +421 248 282 701-2
info@gopas.sk



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